IN THE ABSTRACT OF THE DISCLOSURE

Please amend the Abstract as follows:

Systems, methods, and apparatus extract and combine ~~A method and computer program for extracting and combining~~ arithmetic flags utilized in the processing of multiple data items in a single instruction multiple data (SIMD) capable processor. In an [[a]] SIMD processor several pieces of data may be manipulated by the same instruction at any given moment. However, the results for the execution of this instruction vary according to the data being manipulated. Embodiments provide a ~~method and computer program allows a simple~~ mechanism in which these arithmetic flags may be ~~maybe~~ extracted and combined so as to maximize processor efficiency while saving space, and reducing power requirements and heat generated by the processor.